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BIBDATASHEET

CONFIRMATION NO. 1

Bib Data Sheet

SERIAL NUMBER 09/863,650	FILING DATE 05/22/2001 RULE	CLASS 708	GROUP ART UNIT 2124	ATTORNEY DOCKET N 9818-0038-999
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APPLICANTS

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** CONTINUING DATA *****

This appln claims benefit of 60/236,244 09/28/2000
lu

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 07/24/2001

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 6	TOTAL CLAIMS 26	INDEPENDENT CLAIMS 8
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature <i>lu</i> Initials				

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TITLE

Apparatus and method for implementing efficient arithmetic circuits in programmable logic devices

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